L Number	Hits	Search Text	DB	Time stamp
26	19	uncover\$4 adj code	USPAT;	2004/05/17 17:10
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
27	382762	finger rake	USPAT;	2004/05/17 17:11
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	İ
28	2	(uncover\$4 adj code) and (finger rake)	USPAT;	2004/05/17 17:20
			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
29	1976	walsh adj code	USPAT;	2004/05/17 17:22
			US-PGPUB;	
			EPO; JPO;	
		•	DERWENT	
30	25	(walsh adj code) near3 (finger rake)	USPAT;	2004/05/17 17:22
1			US-PGPUB;	
			EPO; JPO;	
			DERWENT	
31	4	((walsh adj code) near3 (finger rake)) and	USPAT;	2004/05/17 17:22
j i		uncover\$4	US-PGPUB;	
			EPO; JPO;	
			DERWENT	

Search History 5/17/04 5:28:27 PM Page 1

US-PAT-NO:

5764687

DOCUMENT-IDENTIFIER:

US 5764687 A

TITLE:

Mobile demodulator architecture for

a spread spectrum

multiple access communication system

----- KWIC -----

Brief Summary Text - BSTX (36):

The output of the integrator controls a decimator that selects one of the

input samples over a chip interval to use in demodulation. If a peak moves,

the finger adjusts its decimator position to move with it. The decimated

sample stream is then despread with the PN sequence consistent with the offset

to which the finger is assigned. The despread I and Q samples are summed over

a symbol to produce a pilot vector (P.sub.I, P.sub.Q).

These same despread I

and Q samples are Walsh uncovered using the Walsh code assignment unique to the

mobile user and the uncovered, despread I and Q samples are summed over a

symbol to produce a symbol data vector (D.sub.I, D.sub.Q). The dot product

operator is defined as

Detailed Description Text - DETX (8):

The Walsh code is conveyed to the finger through microprocessor databus 34.

The despread and uncovered I and Q chips are summed by I and Q symbol $\$

accumulators 110 and 112 over a symbol interval, once per symbol, producing a

symbol data pair D.sub.I (n) and D.sub.Q (n) for symbol n. Since the pilot

channel is covered with the all zeroes Walsh code 0, no separate Walsh sequence

generator is needed to recover the pilot. The output of

on-time despreader is directly summed by on-time I and Q accumulators 114 and 116, producing a pilot pair P.sub.I (n) and P.sub.Q (n) for symbol n.

Claims Text - CLTX (44):

an uncover circuit, coupled to the Walsh chip sequence generator, for reversing orthogonal covering of the first despread I and Q signals in response to the Walsh chip sequence;

Claims Text - CLTX (45):

a plurality of accumulators, a first accumulator coupled to the first despread I signal, a second accumulator coupled to the first despread Q signal, a third accumulator coupled to the second despread I signal, a fourth accumulator coupled to the second despread Q signal, and a fifth and sixth accumulator coupled to the uncover circuit, the plurality of accumulators summing their respective I or Q signals;